Products

SUMMARY OF TEST REPORT NO : ULR No.: Dated:	Number of pages in test report: Page 1 to 23
TEST FORMAT AS PER IS 16169: 2019 / IEC 62116: 2014	
Name of Manufacturer :	
Product : Utility-Interconnected - Photovoltaic Inverters	
Model(s) :	
4.Model differences provided (if applicable) : Yes ☐ or No ☐ or N/A ☐	
5.Model differences verified as per MNRE Guidelines for series formulation : Yes ☐ or No ☐ or N/A ☐	
6.Test Result: See below Decision Rule: The laboratory employs simple acceptance rule in making Pass or fail decisions on test results with no guard band.	

Discipline:

Group:

SL. NO'	TEST REQUIREMENT	CLAUSE	VERDICT
1	Test for single or multi-phase inverter	6.0	
2	Islanding as it applies to PV systems (Informative)	Annex A	
3	Test for independent islanding detection device	Annex B	

Products

General Information:

1. The conformity certificates of critical components are verified to ensure complete compliance of apparatus under test and details regarding harmonized IEC standards (where IEC standards are not available) are also provided in the list of critical component.

Abbreviations: P = Pass N/A = Not Applicable

CONCLUSION:

I, hereby, undertake that the verdict stated in the test reports for all the tests matches with the test results.

1. Sample meets all relevant requirements of *IS* 16169: 2019/ IEC 62116: 2014. *Yes* ☐ *or No* ☐
2. Sample fails to meet the following test requirements: *Yes* ☐ *or No* ☐ :
3. If any deviation is found, suitable punitive action may be taken by BIS.

Date: (Signature of Authorized person with Stamp)

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TEST REPORT **IS 16169: 2019/ IEC 62116: 2014** **Test Procedure of Islanding Prevention Measures for** **Utility-Interconnected - Photovoltaic Inverters**	
Report Reference No. ..... :	
Date of issue ..... :	
Total number of pages .....:	
Testing Laboratory.....: Address .....:	
Applicant's name .....:	
Address.....:	
Test specification .....:	
Standard .....:	IS 16169: 2019/ IEC 62116: 2014
Test procedure .....:	BIS Compliance Report
Non-standard test method .....:	N/A
Test Report Form No. .....:	
Test Report Form Originator.....:	
Master TRF.....:	
Test item description.....:	Utility-Interconnected - Photovoltaic Inverters
Trade Mark .....:	

Manufacturer .....:	
Factory.....:	
Model/Type reference.....:	
Ratings.....:	
☐ **Testing Laboratory.....**: Testing location/ address .....:	
Tested by (name + signature) .....:	
Approved by (name + signature) .....:	
Summary of testing:	
Tests performed (name of test and test clause):	**Testing location:**

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Test for single or multi-phase inverter	6.0	
Documentation	7.0	
Unintentional Islanding		

Copy of marking plate of the equipment: (Representative Models)	

Copy of marking plate of the equipment: Series Models	
The marking plate above represents all models covered by this report except for difference in electrical ratings and model designation. See “General product information” for electrical ratings for all models.	

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

GENERAL INFORMATION	
Test item particulars:	Utility-Interconnected - Photovoltaic Inverters
Accessories and detachable parts included in the evaluation.....:	N/A
Options included.....:	N/A
Possible test case verdicts:	See below
Abbreviations used in the report:	See below
EUT – Equipment Under Test	MPPT – Maximum Power Point Tracking
Qf – Quality factor	W - Utility Real Power
Var – Utility Reactive Power	V_{DC} – DC Voltage
VEUT – AC Voltage of EUT	t_R – Run on time
IR – Resistive load current	I_L–Inductive load current
IC – Capacitive load current	P_{AC} – Utility Real Power
QAC – Utility Reactive Power	I_{AC} – Utility Current
Possible test case verdicts: - test case does not apply to the test object .....:	 N/A
- test object does meet the requirement.....:	Pass (P)
- test object does not meet the requirement.....:	Fail (F)

Testing Date of receipt of test item.....: Date(s) of performance test.....:	
Manufacturer's Declaration per Standard: **Similarities between the models:** **Differences between the models:** **Model No. tested with-in the family series: Lead Model**	

General remarks:

The test results presented in this report relate only to the object tested.
This report shall not be reproduced, except in full, without the written approval of the Issuing testing laboratory.
"(see Enclosure #)" refers to additional information appended to the report.
"(see appended table)" refers to a table appended to the report.
Throughout this report a point is used as the decimal separator.

Decision Rule: The laboratory employs simple acceptance rule in making Pass or fail decisions on test results with no guard band.

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IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

General Product Information:

Product Electrical Ratings

Parameter	Value		Remarks
1) Rating			
a) Maximum output power	VA		
b) DC voltage range	Vdc		
c) DC current limits	Idc		
d) AC voltage range	Vac		
e) Frequency range	Hz		
f) AC current limits	A		
g) Efficiency	%		
h) Voltage trip settings (magnitude and timing)	V		
i) Frequency trip settings (magnitude and timing)	Hz		
i) Other software settings	---		
j) Firmware version	---		
2) Others	---		
a) Displays	---		
b) Temperature range	°C		
c) Humidity	%		
d) Size	mm		
e) Weight	Kg		

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict
4	Testing circuit		
	The testing circuit shown in Figure 1 is employed.		
	Similar circuits are used for three-phase output.		
	Parameters to be measured are shown in Table 1 and Figure 1. Parameters to be recorded in the test report are discussed in Clause 7.		
5	Testing equipment		
5.1	Measuring instruments		
	The waveform measurement/capture device is able to record the waveform from the beginning of the islanding test until the EUT ceases to energize the island.		
	For multi-phase EUT, all phases are monitored.		
	A waveform monitor designed to detect and calculate the run-on time may be used.		
	For multi-phase EUT, the test and measurement equipment is recorded each phase current and each phase-to-neutral or phase-to-phase voltage, as appropriate, to determine fundamental frequency active and reactive power flow over the duration of the test.		
	A sampling rate of 10 kHz or higher is recommended. The minimum measurement accuracy is 1 % or less of rated EUT nominal output voltage and 1 % or less of rated EUT output current		
	Current, active power, and reactive power measurements through switch S1 used to determine the circuit balance conditions report the fundamental (50 Hz or 60 Hz) component.		
5.2	DC power source		
5.2.1	General		
	A PV array or PV array simulator (preferred) may be used. If the EUT can operate in utility- interconnected mode from a storage battery, a DC power source may be used in lieu of a battery as long as the DC power source is not the limiting device as far as the maximum EUT input current is concerned.		
	The DC power source provides voltage and current necessary to meet the testing requirements described in Clause 6.		
5.2.2	PV array simulator		
	The tests are conducted at the input voltage defined in Table 2 below, and the current is limited to 1,5 times the rated photovoltaic input current, except when specified otherwise by the test requirements.		
	A PV array simulator is recommended, however, any type of power source may be used if it does not influence the test results.		

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Clause	Requirement + Test	Result - Remark	Verdict
5.2.3	Current and voltage limited DC power supply with series resistance		
	A DC power source used as the EUT input source is capable of EUT maximum input power (so as to achieve EUT maximum output power) at minimum and maximum EUT input operating voltage.		
	The power source provides adjustable current and voltage limit, set to provide the desired short circuit current and open circuit voltage when combined with the series and shunt resistance described below.		
	A series resistance (and, optionally, a shunt resistance) is selected to provide a fill factor within the range: Output power: Sufficient to provide maximum EUT output power and other levels specified by test conditions of table 5. Response speed: The response time of a simulator to a step in output voltage, due to a 5% load change, results in a settling of the output current to within 10% of its final value in less than 1ms. Stability: Excluding the variations caused by the EUT MPPT, simulator output power remains stable within 2 % of specified power level over the duration of the test: from the point where load balance is achieved until the island condition is cleared or the allowable run-on time is exceeded. Power factor: 0.25 to 0.8		
5.2.4	PV array		
	A PV array used as the EUT input source is capable of EUT maximum input power at minimum and maximum EUT input operating voltage.		
	Testing is limited to times when the irradiance varies by no more than 2 % over the duration of the test as measured by a silicon-type pyranometer or reference device. It may be necessary to adjust the array configuration to achieve the input voltage and power levels prescribed in 6.1.		
5.3	AC power source		
	The utility grid or other AC power source may be used as long as it meets the conditions specified in Table 4. **Table 4 – AC power source requirements** **Items** / **Conditions** Voltage — Nominal ±2,0 % Voltage THD — < 2,5 % Frequency — Nominal ±0,1 Hz Phase angle distance [1] — 120 ° ± 1,5 ° [1] Three-phase case only		
5.4	AC loads		

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Clause	Requirement + Test	Result - Remark	Verdict
	On the AC side of the EUT, variable resistance, capacitance, and inductance are connected in parallel as loads between the EUT and the AC power source. Other sources of load, such as electronic loads, may be used if it can be shown that the source does not cause results that are different than would be obtained with passive resistors, inductors, and capacitors.		
	All AC loads are rated for and adjustable to all test conditions. The equations for Qf are based upon an ideal parallel RLC circuit. For this reason, non- inductive resistors, low loss (high Qf) inductors, and capacitors with low effective series resistance and effective series inductance are utilized in the test circuit. Iron core inductors, if used, are not exceed a current THD of 2 % when operated at nominal voltage. Load components are conservatively rated for the voltage and power levels expected. Resistor power ratings are chosen so as to minimize thermally-induced drift in resistance values during the course of the test.		
	Active and reactive power is calculated (using the measurements provided in Table 1) in each of the R, L and C legs of the load so that these parasitic parameters (and parasitics introduced by variacs or autotransformers) are properly accounted for when calculating Qf.		
6	Test for single or multi-phase inverter		
6.1	Test procedure		
	The test uses an RLC load, resonant at the EUT nominal frequency (50 Hz or 60 Hz) and matched to the EUT output power.		
	For multi-phase EUT, the load is balanced across all phases and the switch S1 as in Figure 1 opens all phases		
	This test is performed with the EUT conditions as in Table 5, where power and voltage values are given as a percent of EUT full output rating.		
	a) Determine EUT test output power		
	b) Adjusting the DC input source		
	c) Turn off the EUT and open S1		
	d) Adjust the RLC circuit to have Qf = 1.0 ±0.05		
	e) Connect the RLC load configured in step d) to the EUT by closing S2		
	f) Open the utility-disconnect switch S1 to initiate the test, Run-on time is recorded.		
	g) For test condition A, adjust the real load and only one of the reactive load components to each of the load imbalance conditions shown in the shaded portion of table 6. If any of the recorded run-on times are longer than the one recorded for the rated balance condition, then the non-shaded parameter combinations also require testing.		

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict
	h) For test condition B and C, adjust the only one reactive load components by approximately 1,0% per test, within a total range of 95% to 105% of the operating point. If run-on times are still increasing at the 95% or 105% points, additional 1% increments have to be taken until run-on times begin decreasing.		
6.2	Pass/fail criteria		
	An EUT is considered to comply with the requirements for islanding protection when each case of recorded run-on time is less than 2 s or meets the requirements of local codes.		
7	Documentation		
	At a minimum, the following information is recorded and maintained in the test report.		
	a) Specifications of EUT. Table 8 provides an example of the type of information that is provided.		
	b) Measurement results. Table 9 provides an example of the type of information that is provided. Actual measured values is to be recorded.		
	c) Block diagram of test circuit.		
	d) Specifications of the test and measurement equipment. Table 10 provides an example of the type of information that is provided.		
	e) Any test configuration or procedure details such as methods of achieving specified load and EUT output conditions.		
	f) Any additional information required by the testing laboratory's accreditation.		
	g) Specify the evaluation criterion from clause 6.2 that was utilized to determine if the product passed or failed the test.		
Annex A	Islanding as it applies to PV systems (Informative)		
A.1	General		
A.2	Impact of distortion on islanding		
Annex B	Test for independent islanding detection device (relay)(Informative)		
B.1	Introduction		
B.2	Testing circuit		
B.3	Testing equipment		
B.4	Testing procedure		
B.5	Documentation		

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Clause	Requirement + Test	Result – Remark	Verdict

6.1	Table: Tested condition and run-on time											P
Sl no.	PEUT a (% of EUT Rating)	Reactive load (% of QL)	PAC b (% of nominal)	QAC c (% of nominal)	Run on time (ms)	PEUT (KW)	Pr (Resistive) KW	PI (Inductive) Kvar	Pc (Capacitve) Kvar	Actual Qf	VDC	Remarks
1	100	100	0	0								Test A at BL
2	66	66	0	0								Test B at BL
3	33	33	0	0								Test C at BL
4	100	100	-5	-5								Test A at IB
5	100	100	-5	0								Test A at IB
6	100	105	-5	5								Test A at IB
7	100	100	0	-5								Test A at IB
8	100	100	0	5								Test A at IB
9	100	100	5	-5								Test A at IB
10	100	100	5	0								Test A at IB

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Clause	Requirement + Test	Result - Remark	Verdict

11	100	100	5	5								Test A at IB
12	100	100	-10	10								Test A at IB
13	100	100	-5	10								Test A at IB
14	100	100	0	10								Test A at IB
15	100	100	5	10								Test A at IB
16	100	100	10	10								Test A at IB
17	100	100	10	5								Test A at IB
18	100	100	10	0								Test A at IB
19	100	100	10	-5								Test A at IB
20	100	100	10	-10								Test A at IB
21	100	100	5	-10								Test A at IB
22	100	100	0	-10								Test A at IB

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Clause	Requirement + Test	Result - Remark	Verdict

23	100	100	-5	-10								Test A at IB
24	100	100	-10	-10								Test A at IB
25	100	100	-10	-5								Test A at IB
26	100	100	-10	0								Test A at IB
27	100	100	-10	5								Test A at IB
28	66	66	0	-5								Test B at IB
29	66	66	0	-4								Test B at IB
30	66	66	0	-3								Test B at IB
31	66	66	0	-2								Test B at IB
32	66	66	0	-1								Test B at IB
33	66	66	0	1								Test B at IB
34	66	66	0	2								Test B at IB

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

35	66	66	0	3								Test B at IB
36	66	66	0	4								Test B at IB
37	66	66	0	5								Test B at IB
38	33	33	0	-5								Test C at IB
39	33	33	0	-4								Test C at IB
40	33	33	0	-3								Test C at IB
41	33	33	0	-2								Test C at IB
42	33	33	0	-1								Test C at IB
43	33	33	0	1								Test C at IB
44	33	33	0	2								Test C at IB
45	33	33	0	3								Test C at IB

IS 16169: 2019/ IEC 62116: 2014			
Clause	**Requirement + Test**	**Result - Remark**	**Verdict**

46	33	33	0	4								Test C at IB
47	33	33	0	5								Test C at IB

a PEUT: EUT output power.

b PAC: Active power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0% test condition value

c QAC: Reactive power flow at S1 in Figure 1. Positive means power from EUT to utility. Nominal is the 0% test condition value

d BL: balance condition, IB: imbalance condition.

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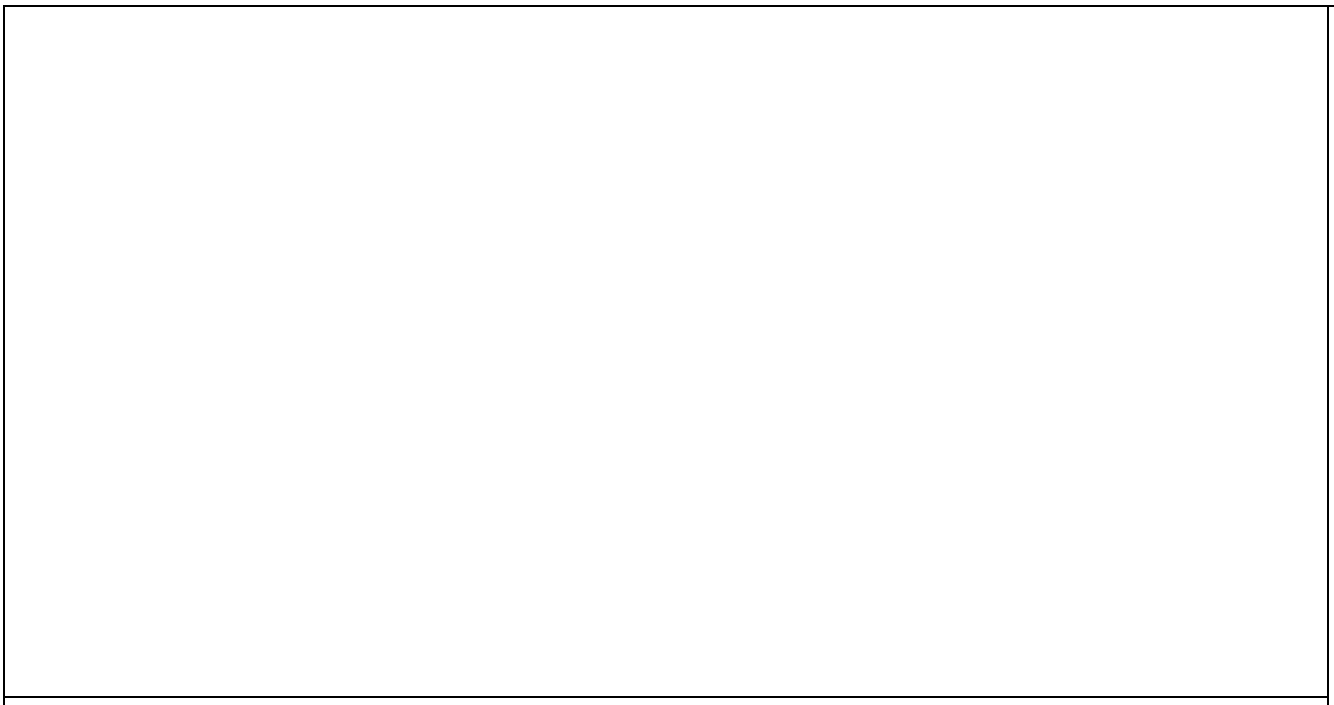
Clause	Requirement + Test	Result - Remark	Verdict

Appendix A
Photographs

For Model:	
	Front and Rear view

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict



Overall view

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

	String Connectors / AC Output
	Protective Ground Symbol

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

	Dimensional View

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

Internal Construction view

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

PCB Front and Rear View

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result – Remark	Verdict

QR code for IS 16221: 2015	QR code for IS 16169 : 2014	
		QR Code

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

Waveforms:

100% Balance Condition [0 0]

100% Imbalance condition [-5 -5]

66% Balance Condition [0 0]

66% Imbalance condition [0 -5]

33% Balance Condition [0 0]

33% Imbalance condition [0 -5]

IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

PRODUCT DATASHEET:

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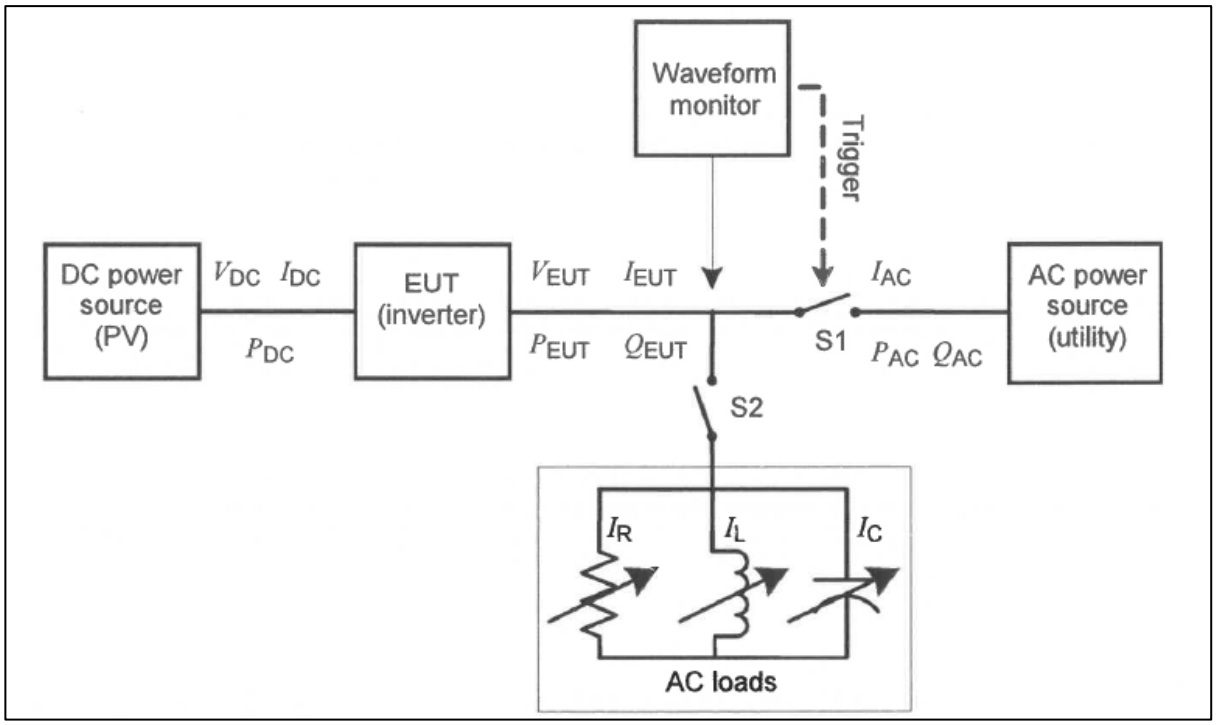
IS 16169: 2019/ IEC 62116: 2014			
Clause	Requirement + Test	Result - Remark	Verdict

TEST CIRCUIT:



---------- End of Test Report ----------

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